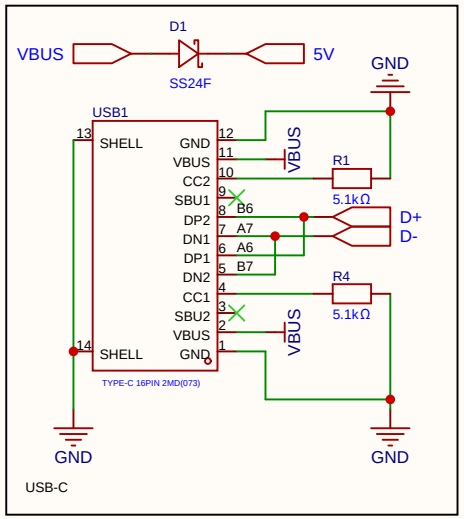
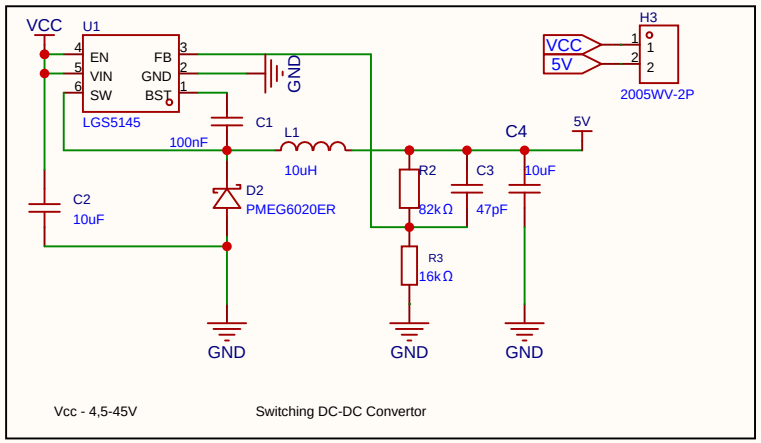
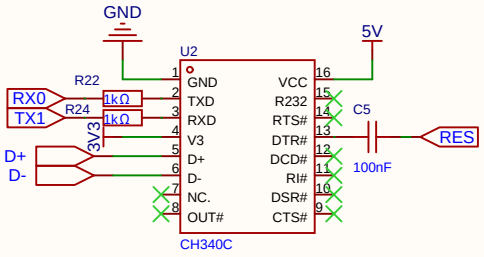
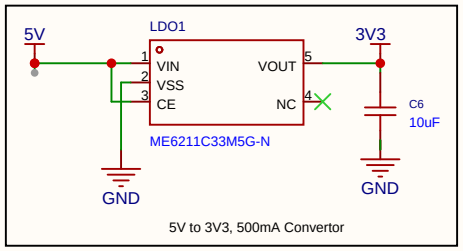
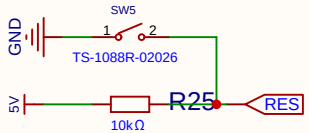
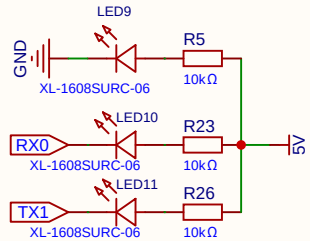


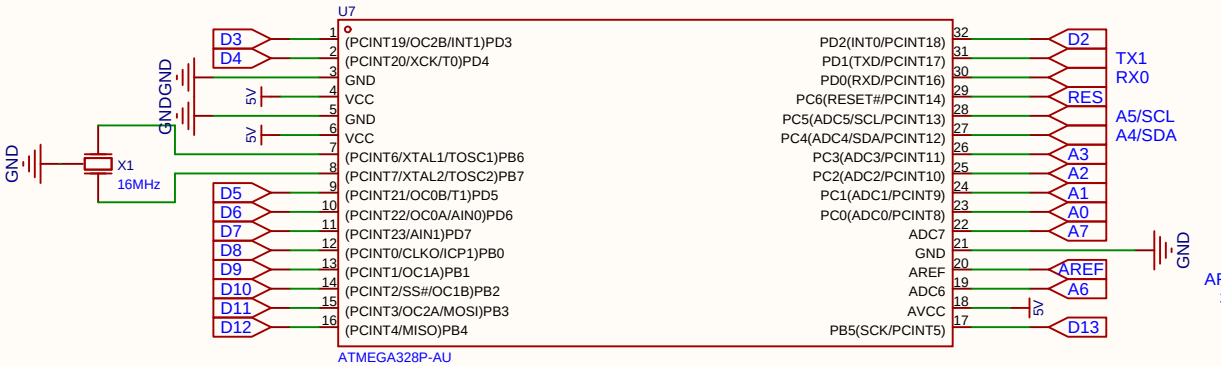



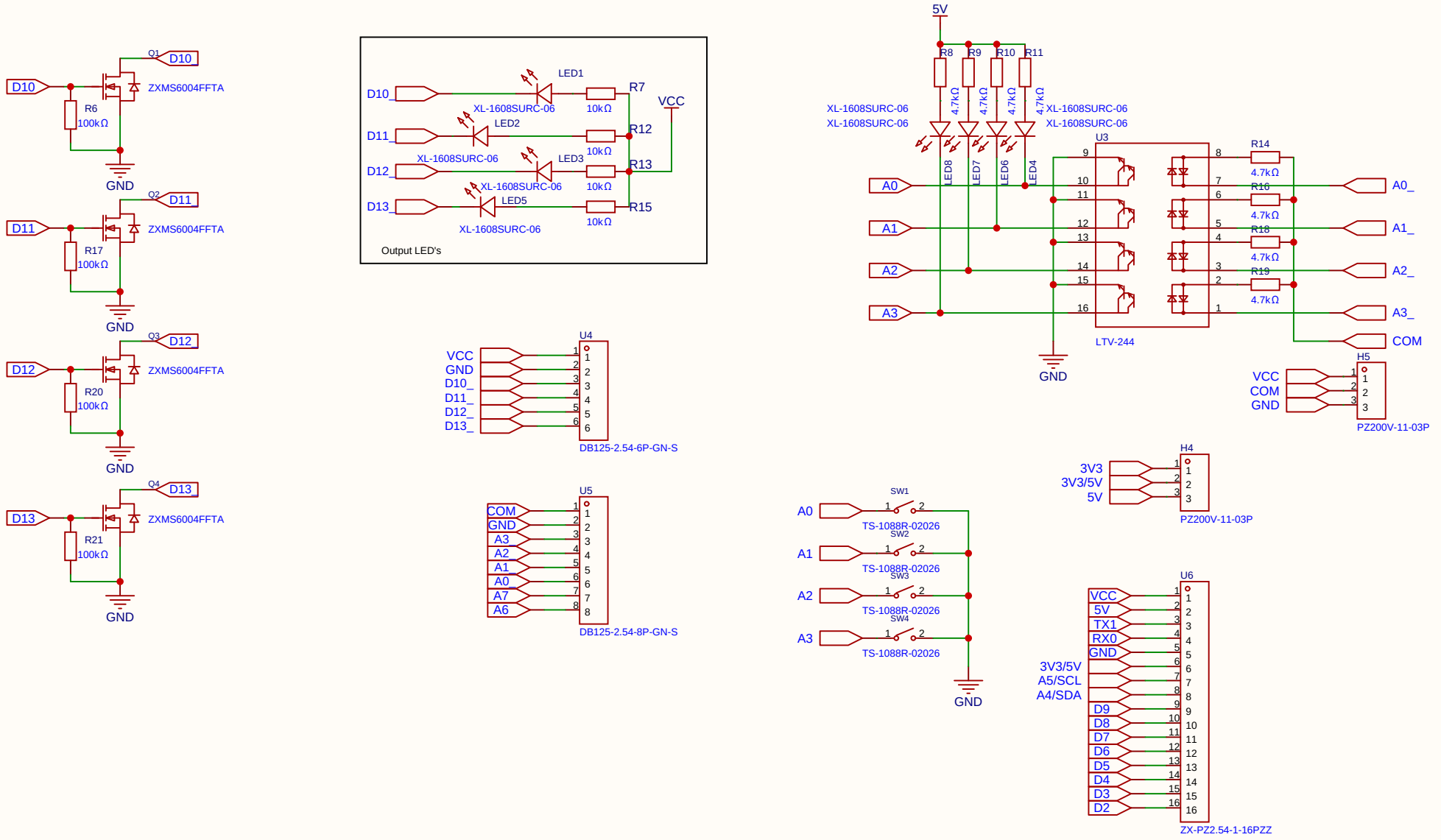



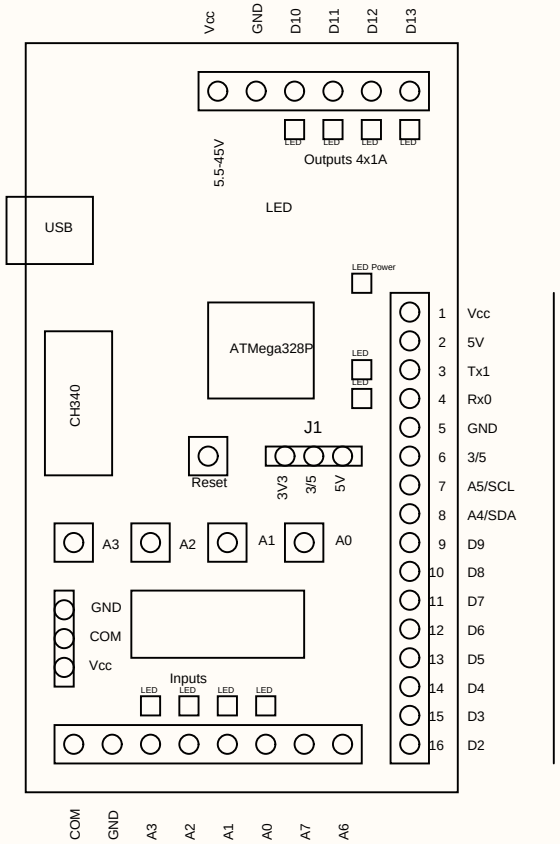

| | | | |
|---|---|---|---|
| Schematic | ATmega328P_PLC | Create at | 2026-01-23 |
| | | Update at | 2026-01-23 |
| Board | Board1 | Page | P1 |
| Drawn | | ATMega328_CH340C | |
| Reviewed | | | |
| | | | |
| | | Version | Size | Page 1 Total 3 |
| EasyEDA | | V1.0 | A4 | PM Engineering |




| | | | |
|---|---|---|---|
| Schematic | ATmega328P_PLC | Create at | 2026-01-23 |
| | | Update at | 2026-01-23 |
| Board | Board1 | Page | P2 |
| Drawn | | ATMega328_CH340C | |
| Reviewed | | | |
| | | Version | Size / Page 2 Total 3 |
| EasyEDA | | V1.0 | A4 / PM Engineering |

The outputs feature integrated transistors rated up to 60V with a 1.3A current limit, and they switch the neutral line.

The supply voltage, Vcc, ranges from 5.5 V to 45 V.




The stabilizer for 5V is 1.0A, and for 3.3V it is 500 mA.

Direct CPU pins

3.3V or 5V can be applied to pin 6,
depending on the setting of jumper J1 next to the pin.

This jumper is used to supply
0 or +Vcc
to COM for convenience.

Inputs (A0 – A3\) feature optocouplers with dual LEDs \(rated up to ±20 mA; they can be controlled using either a positive or negative signal\).

Inputs A7 and A8 are analog-only.

| | | | |
|---|---|---|---|
| Schematic | ATmega328P_PLC | Create at | 2026-02-20 |
| | | Update at | 2026-11-04 |
| Board | Board1 | Page | P3 |
| Drawn | | ATMega328_CH340C | |
| Reviewed | | | |
| | | | |
| | | Version | Size | Page 3 Total 3 |
| EasyEDA | | V1.0 | A4 | PM Engineering |