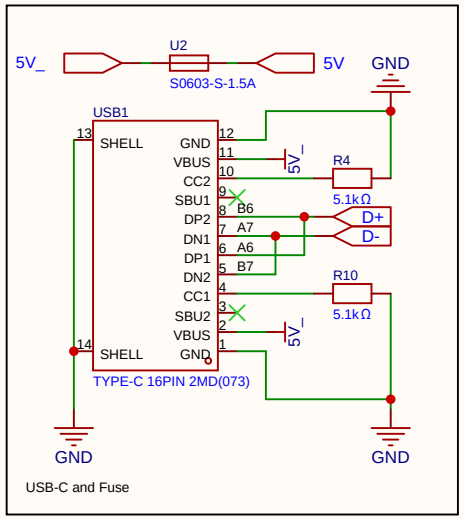
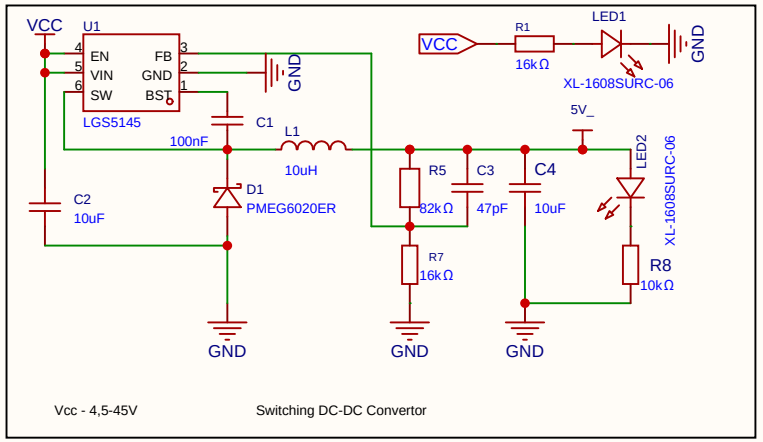
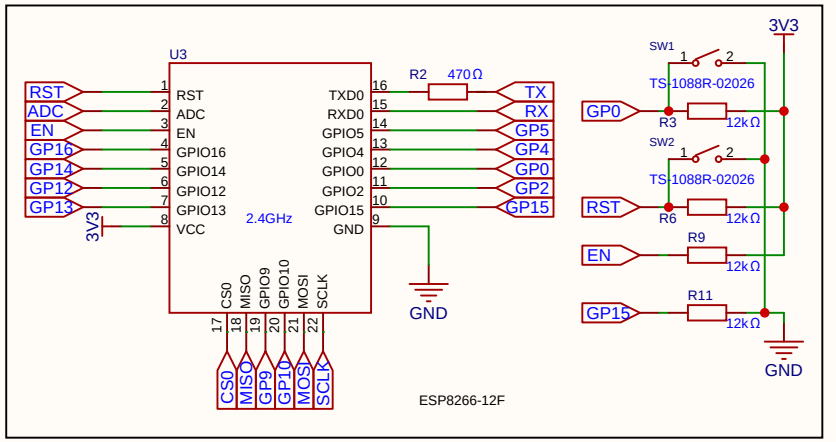
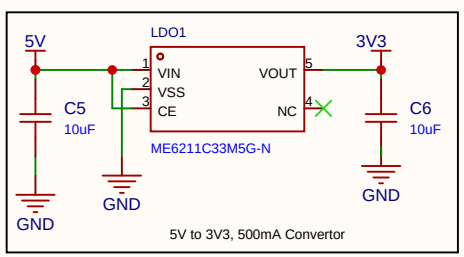
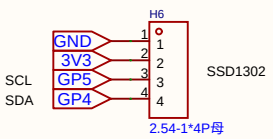
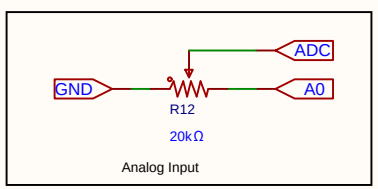


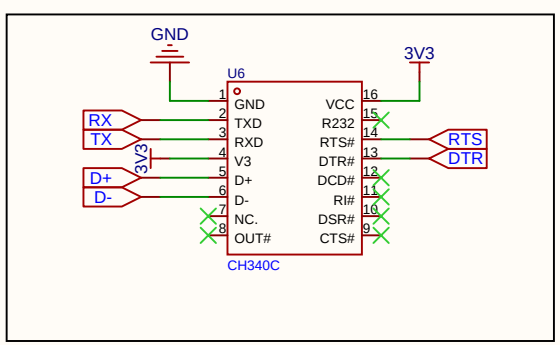


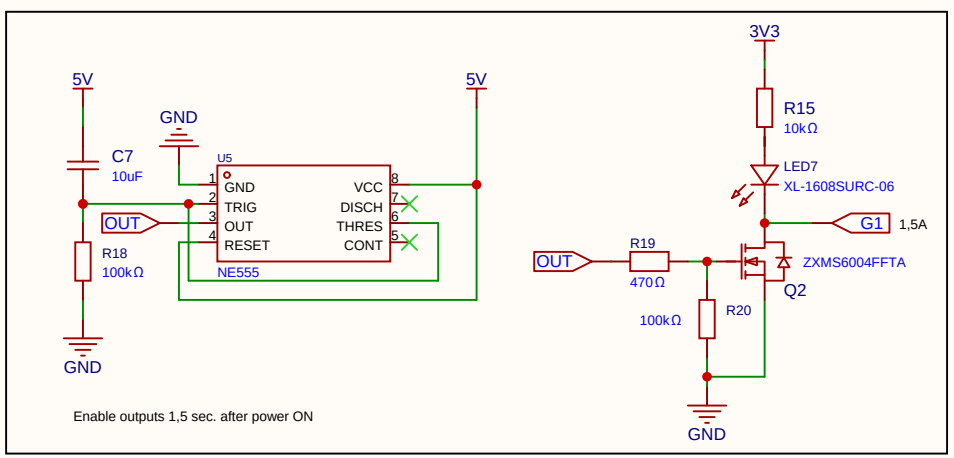


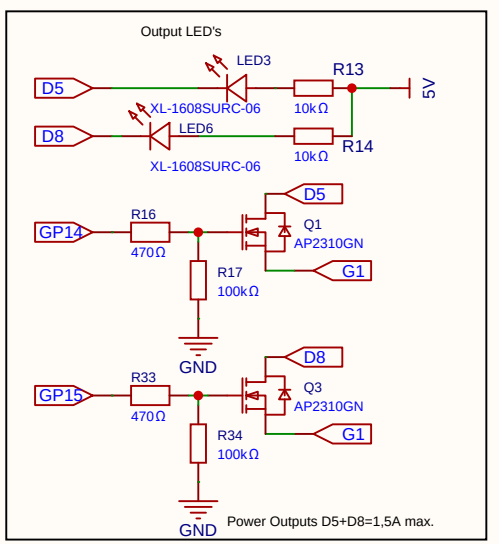


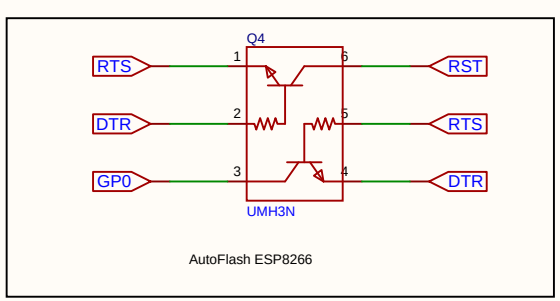




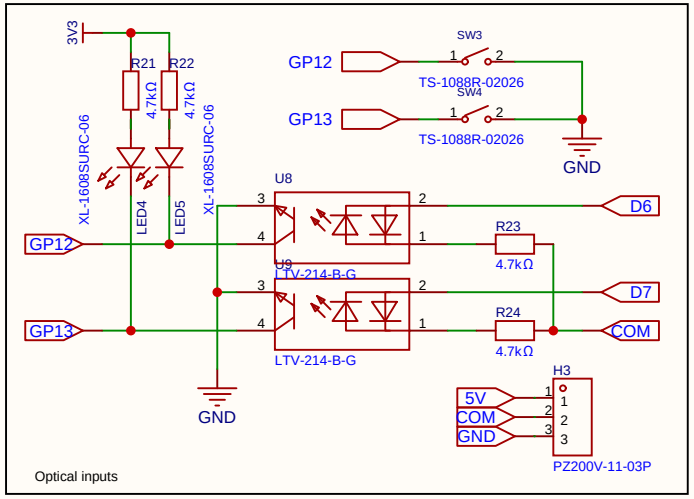


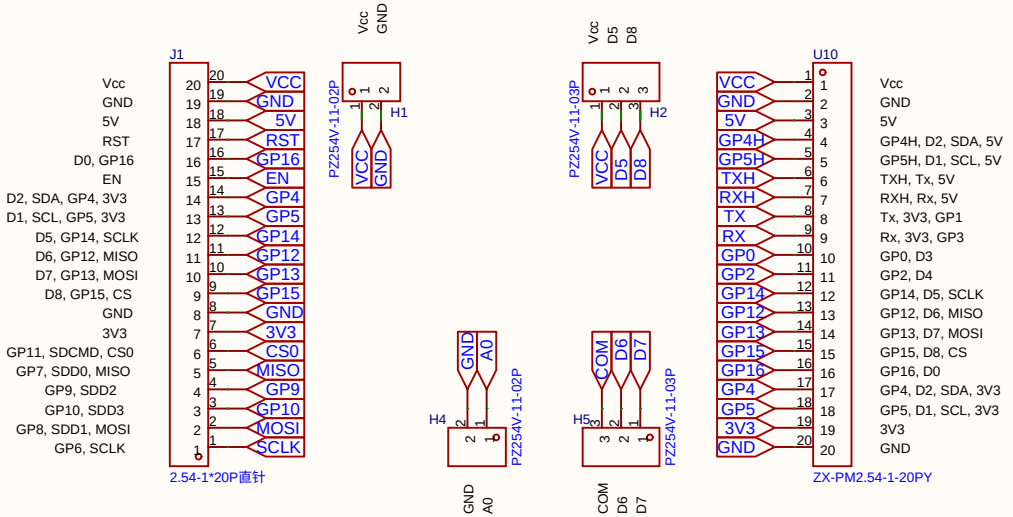


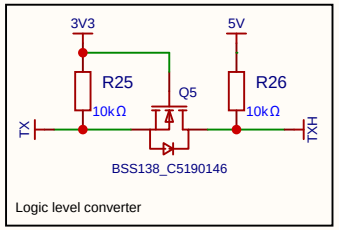


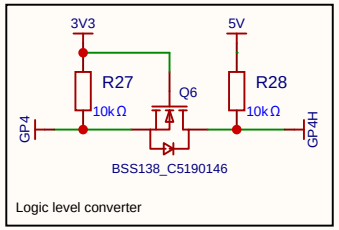


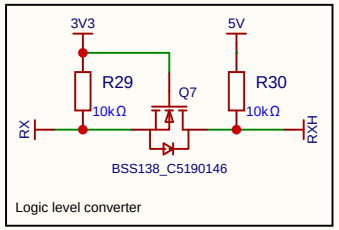
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		Update at	2026-07-08
Board	ESP8266_PLC_V2_0	Page	P1
Drawn		ESP8266_V2_0	
Reviewed			
		Version	Size
EasyEDA		V2.0	A4
		Page 1 Total 5	
		Petar Marinov	

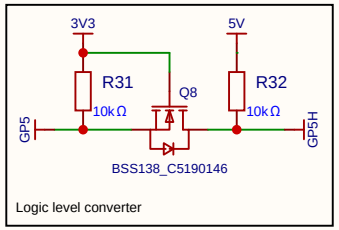










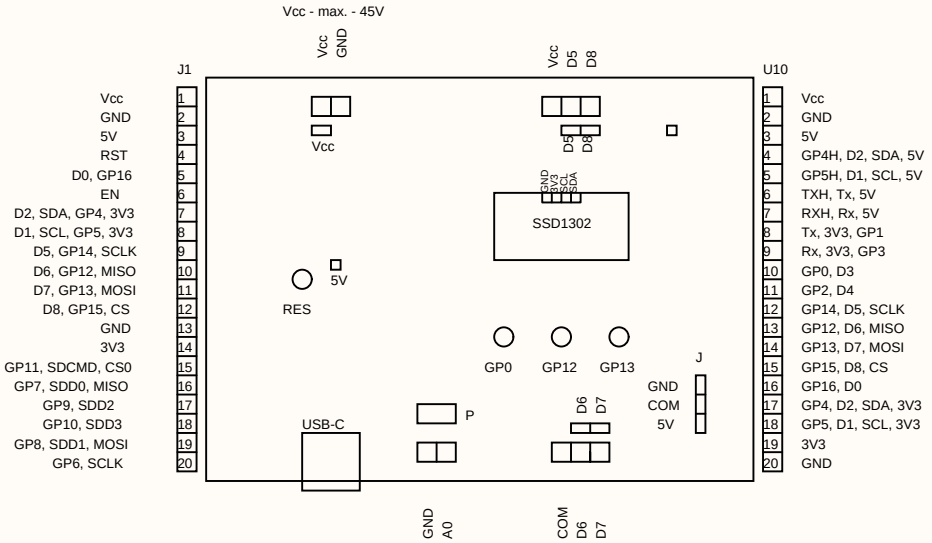


Schematic	ESP8266_PLC_V2_0	Create at	2026-03-19
		Update at	2026-07-08
Board	ESP8266_PLC_V2_0	Page	P2
Drawn		ESP8266_V2_0	
Reviewed			
		Version / Size	Page 2 Total 5
EasyEDA	V2.0 / A4	Petar Marinov	

Има предпазоител 1,5 А на 5V.
There is a 1.5 A fuse on the 5V line.

5V - 1,5A
3V3 - 500 mA

D5, D6 Изходи с NMOS (прекъсват нулата) транзистори до 1,5 А общо за двата изхода.
D5, D6: NMOS transistor outputs (switching the neutral line), with a combined rating of up to 1.5 A for both outputs.



С джъмпера J(H3) може да се подава 0 или 5V към COM за входовете
Jumper J(H3) allows 0V or 5V to be applied to COM for the inputs.

С тримера P(R12) се настройва напрежението на аналоговия вход
The voltage at the analog input is adjusted using the P(R12) trimmer.

На дясната букса са предвидени пинове с конвертори на логически нива за Rx, Tx и SDA, SCL за устройства работещи на 5V.

The right-hand connector features pins with logic-level converters for Rx, Tx, SDA, and SCL, designed for 5V devices.

На пиновете с конвертори на логически нива могат да се подават както 5V сигнали така и 3,3V сигнали. На CPU винаги ще са 3,3V

Pins equipped with logic-level converters can accept both 5V and 3.3V signals.
At the CPU, the signals will always be 3.3V.

Schematic	ESP8266_PLC_V2_0	Create at	2026-04-27
		Update at	2026-10-29
Board	ESP8266_PLC_V2_0	Page	P3
Drawn		ESP8266_V2_0	
Reviewed			
		Version / Size / Page 3 Total 5	
EasyEDA		V2.0 / A4	Petar Marinov