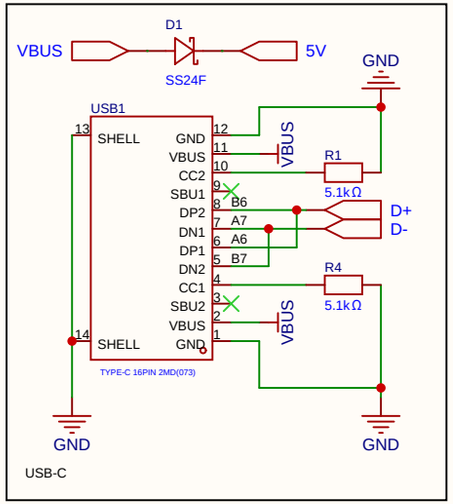
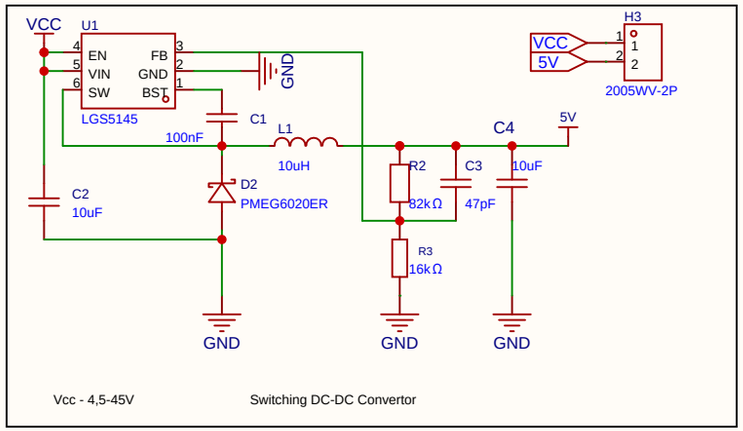
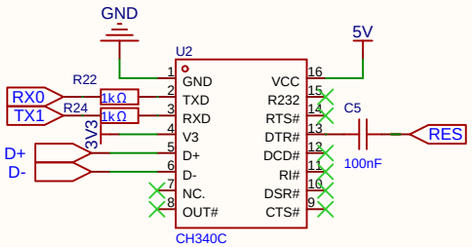
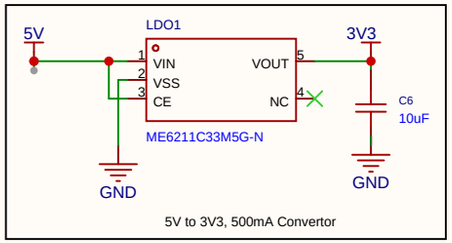
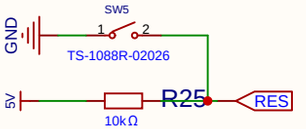
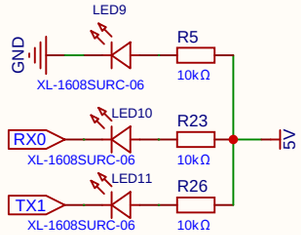






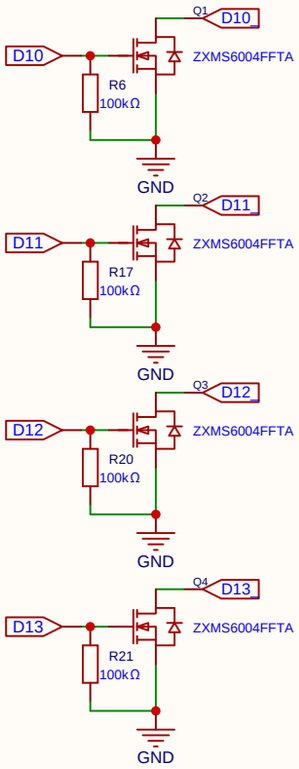


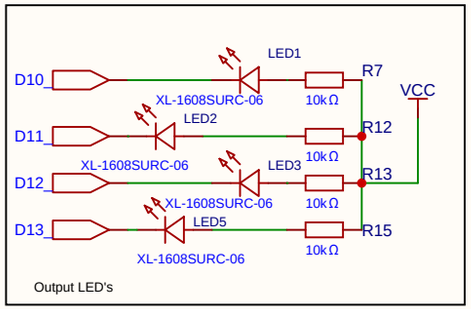


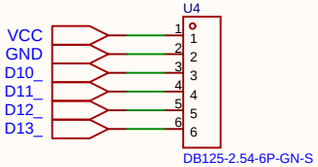


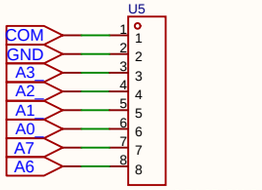


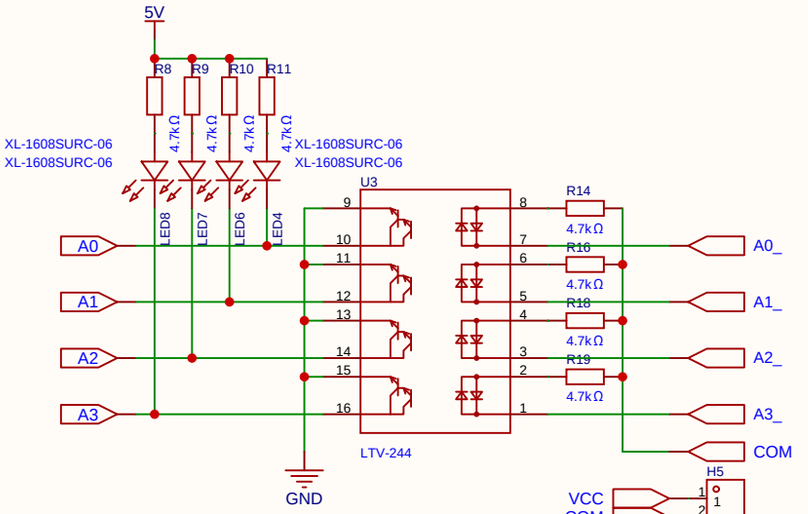
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Board	Board1	Page	P1
Drawn		ATMega328_CH340C	
Reviewed			
	Version	Size	Page 1 Total 3
EasyEDA	V1.0	A4	PM Engineering

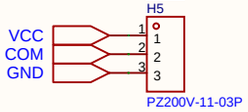


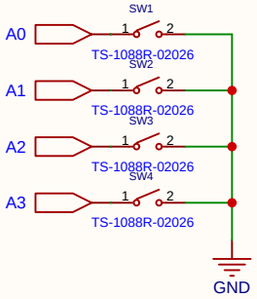


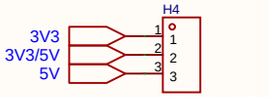


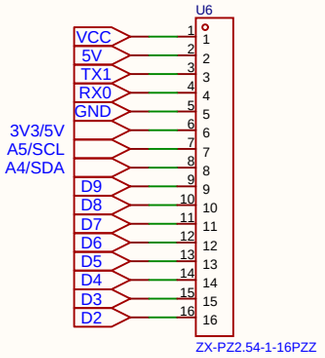








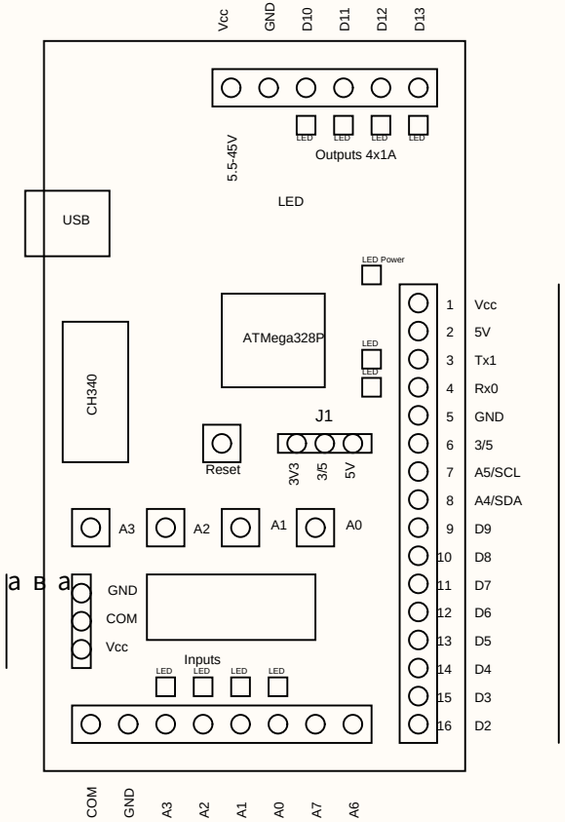




Schematic	Schematic1	Create at	2026-01-23
		Update at	2026-01-23
Board	Board1	Page	P2
Drawn		ATMega328_CH340C	
Reviewed			
	Version	Size	Page 2 Total 3
EasyEDA	V1.0	A4	PM Engineering

Изходите са с интел. транзистори до 60V и токово ограничение 1.3A и комутират нулата.

Захранващото напр. Vcc е от 5,5 до 45V.



Стабилизатора за 5V е 1,5A, а за 3,3V е 500 mA.

Директни пинове на CPU

На 6 може да се подава 3.3 или 5V в зависимост от джъмпера J1 срещу пина.

С този джъмпер се подава 0 или +Vcc на COM за удобство.

Входове (A0-A3) са с оптрони с двойни светодиоди до +- 20mA (могат да се управляват с + или -).

Входове A7 И A8 са само аналогови

Schematic	Schematic1	Create at	2026-02-20
		Update at	2026-02-20
Board	Board1	Page	P3
Drawn		ATMega328_CH340C	
Reviewed			
		Version	Size	Page 3 Total 3
EasyEDA		V1.0	A4	PM Engineering